

FIG. 1

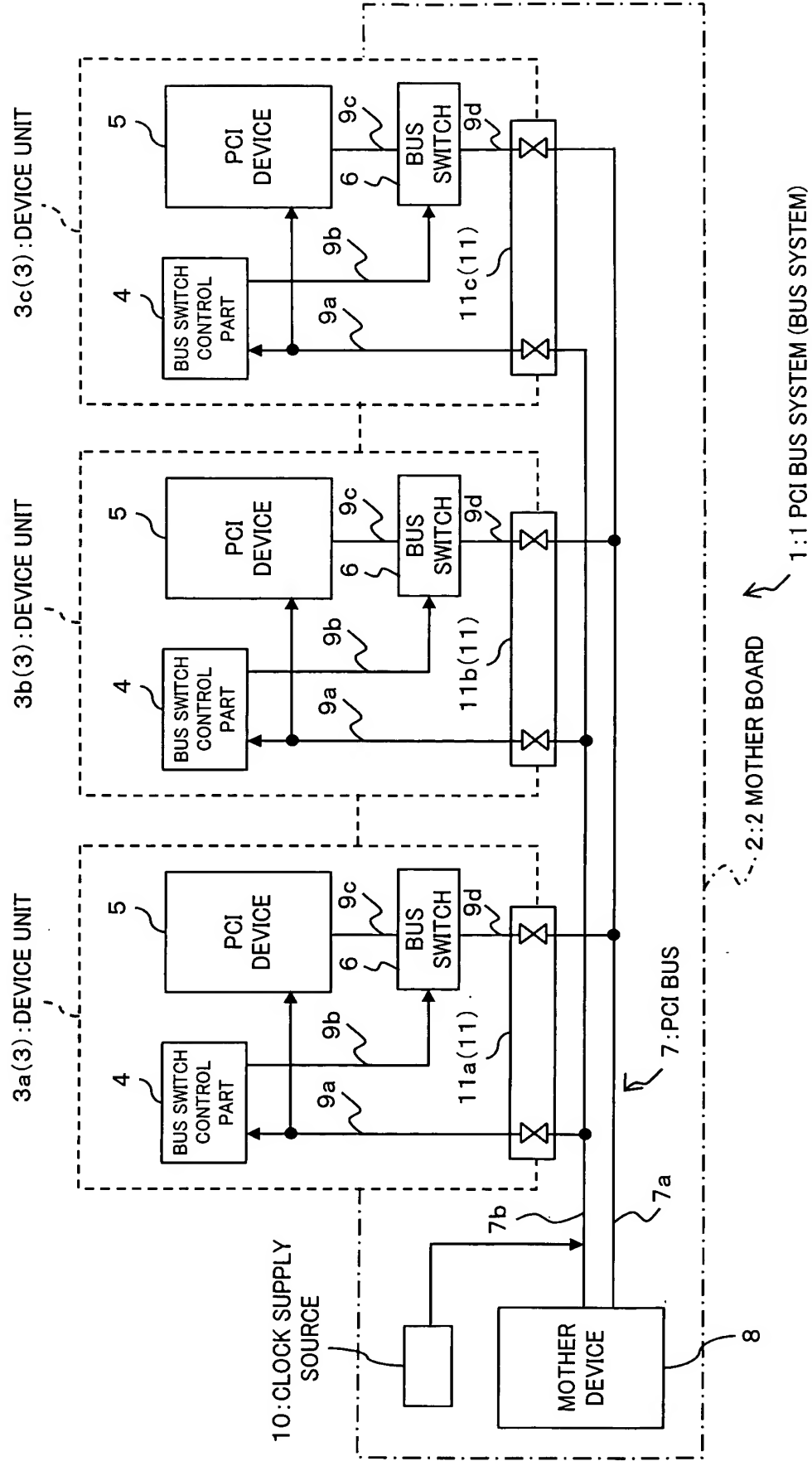


FIG. 2

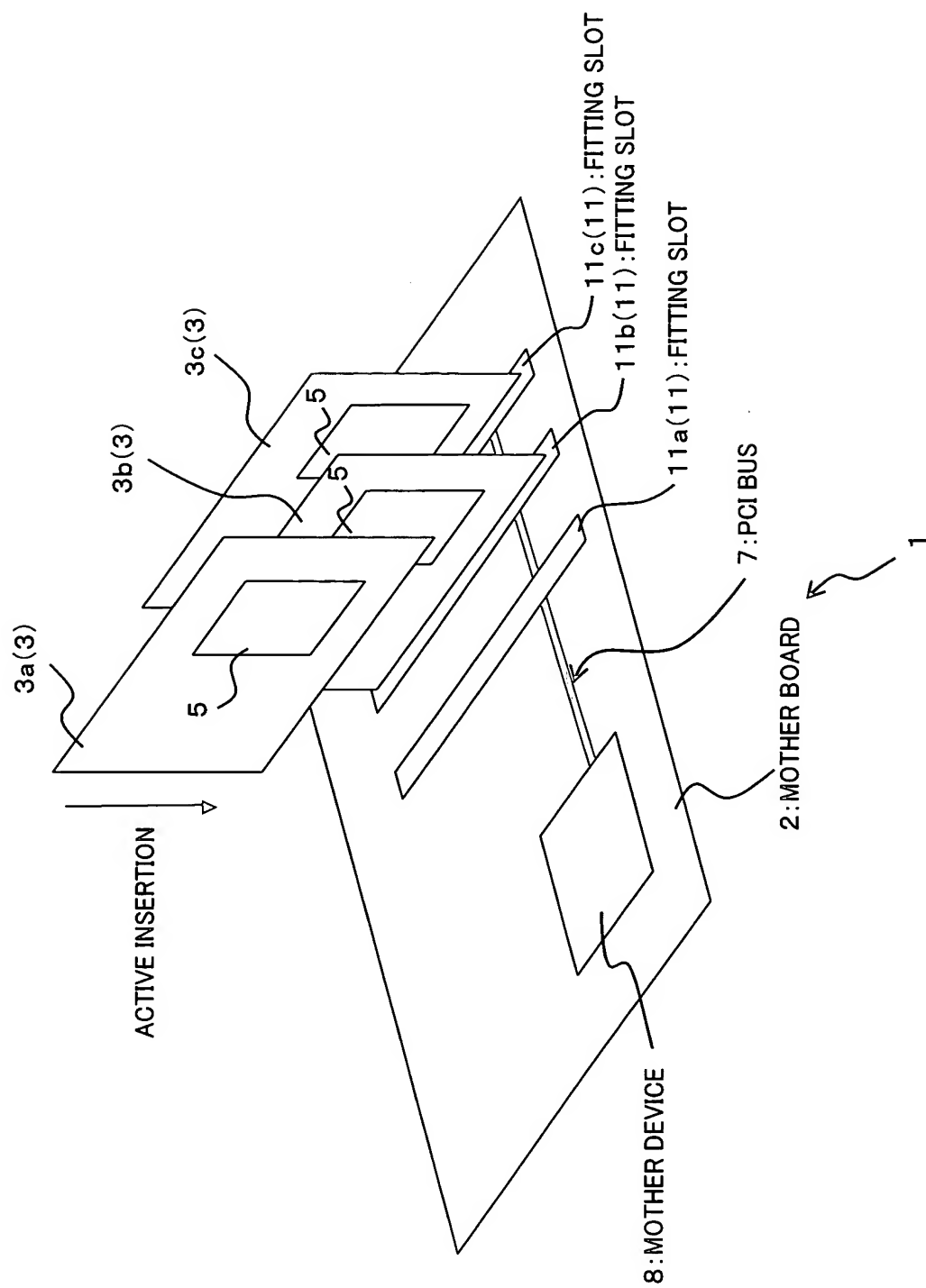


FIG. 3A BASE CLOCK

FIG. 3B INPUT CLOCK TO
BUS SWITCH CONTROL PART

FIG. 3C BUS SWITCH CONNECTION INSTRUCTION OUTPUT

FIG. 3D BUS SWITCH ENABLED

FIG. 3E NOISE OCCURRENCE

FIG. 3F NOISE PROPAGATION

FIG. 3G INPUT CLOCK TO ANOTHER DEVICE UNIT

